**PCB CHECK LIST\_ REV.5**

Check list for the PCB: DER628\_REVA.brd\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Design by: \_\_GJC\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

PI Logo 

Add Family name example: LinkSwitchTM-PH for InnoSwitch TM-CH,CV,RC,MK,EP.

Board name RD-xxx or DER-xxx

Revision Rev xxx

Date code 1125 (year week)

Serial number S/N

Correct Rev. (of PCB design) being sent out.

Primary to Secondary safety isolation being met. (for 6.8mm spacing)

Add SST for Jumper that uses the TOP layer trace.

Copper pour and make certain that it is attached to the net list.

AC input connector add silk screen L for line, and N for Neutral, PE for Protective Earth.

Output connector add silk screen + (Voltage) V and RTN or Vo and 0v

Transformer Flying Lead add silk screen FL(x)…FL(y)

Solder mask on spark gap in Rectangle shape (6.4mm spacing) on the corrected layer.

Added note to show the slot location using Drill Drawing Layer.

Board Dimension.

Board Material, Copper, thickness.

No silkscreen on copper pads

Responsible Engineers Approval (Type name) \_Clomiferds Sales\_

**For Green Board Fabrication (Need Approval from Kevin)**

Approved by \_\_\_Clomiferds Sales\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Date \_\_\_\_\_\_08-03-2017\_\_\_4:00pm\_\_\_\_\_\_\_\_\_\_\_\_\_\_